312 707 9155 P.04/17

7/31/02

Attorney Docket No. 13438US01

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Wakayama et al.

Serial No.:

09/456,230

Filed:

December 7, 1999

For:

LOW JITTER HIGH PHASE RESOLUTION PLL-BASED TIMING RECOVERY SYSTEM

Group Art Unit:

2816

Examiner:

A. Tra

EXPRESS MAIL

Express Mail Label No. EL283615795US

Date: January 9, 2002

Jeffrey D. Wheeler

Reg. No. 39,066

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JUL 3 1 2002

**TECHNOLOGY CENTER 2800** 

## RESPONSE TO FINAL OFFICE ACTION

BOX AMENDMENT Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Applicants submit this amendment in response to the Office Action dated July 13, 2001, along with a petition and fee for a three-month extension of time in which to respond.

## IN THE CLAIMS:

Please delete Claims 3 and 19.

8. (Twice Amended) A phase lock loop comprising:

a detector for comparing a phase or frequency characteristic of an input signal to a phase or frequency characteristic of a timing reference signal;

a timing reference signal generator, connected in feedback fashion to provide a timing reference signal to the detector;